

Electronic Acknowledgement Receipt

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Application Number:	10804452
International Application Number:	
Confirmation Number:	7279
Title of Invention:	Memory expansion and integrated circuit stacking system and method
First Named Inventor/Applicant Name:	Russell Rapport
Customer Number:	26201
Filer:	David M. Hoffman/Kathy Gilbert
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Attorney Docket Number:	21260-001001
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RAM confirmation Number	1300
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1	Information Disclosure Statement (IDS) Filed	21260021001IDS.pdf	1367700	no	6
Warnings:					
Information:					
This is not an USPTO supplied IDS fillable form					
2	NPL Documents	JEDECDesignRequirements.pdf	3634119	no	22
Warnings:					
Information:					
3	Foreign Reference	JP60254762.pdf	799804	no	5
Warnings:					
Information:					
4	Foreign Reference	JP200088921.pdf	25269759	no	26
Warnings:					
Information:					
5	NPL Documents	PCTUS050756.pdf	816571	no	4
Warnings:					
Information:					
6	NPL Documents	PCTUS0539307.pdf	2779550	no	13
Warnings:					
Information:					
7	NPL Documents	PCTUS0617015.pdf	1654993	no	9
Warnings:					
Information:					
8	NPL Documents	PCTUS2005013336.pdf	2189425	no	10
Warnings:					
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9	NPL Documents	PCTUS2005013345.pdf	1683691	no	8
Warnings:					
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10	NPL Documents	PCTUS2005016764.pdf	1520059	no	8
Warnings:					
Information:					
11	NPL Documents	PCTUS200501075620061012.pdf	921783	no	6
Warnings:					
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12	NPL Documents	RigidFlexArticle.pdf	1599636	no	4
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Information:					
13	NPL Documents	WilliamNewberryDesignTechforBallGrid.pdf	1606217	no	6
Warnings:					
Information:					
14	Foreign Reference	WO9744824.pdf	3115885	no	22
Warnings:					
Information:					
15	Fee Worksheet (PTO-06)	fee-info.pdf	8189	no	2
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If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

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